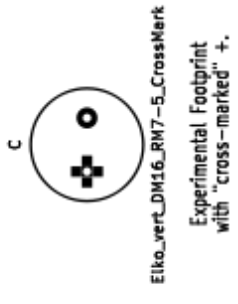


Hint/Warning: The Plus symbol "+" is at the silk screen AND copper layer, too! This may cause some problems with isolations or shortcuts, because a grafical line at a copper layer is NOT recognised by the DRC! If you want to avoid this problems, choose the modules with the suffix "_CopperClear" in the name. There the "+" symbol is only at the silk screen layer.

But if you get boards without silk screen layer, you will have no orientation information!



1:1 Print! Check dimmension by controlling the gauge!

KiCAD Footprints of some vertical mounted through hole electrolytic capacitors from the file Elko_ThroughHole_RevB-3_30Dec2011.mod

Author: Oliver Kirsch + Bernd Wiebus / Uedem / Germany / 07 Feb 2011



GNU-GPL NO WARRANTY!

File: Elko_ThroughHole_RevB-3_30Dec2011.brd	
Sheet: 1/1	
Title:	
Size: A4	Date: 25 apr 2012
KiCad E.D.A.	pcbnew (2012-01-19 BZR 3256) - stable
Rev:	Id: 1/1