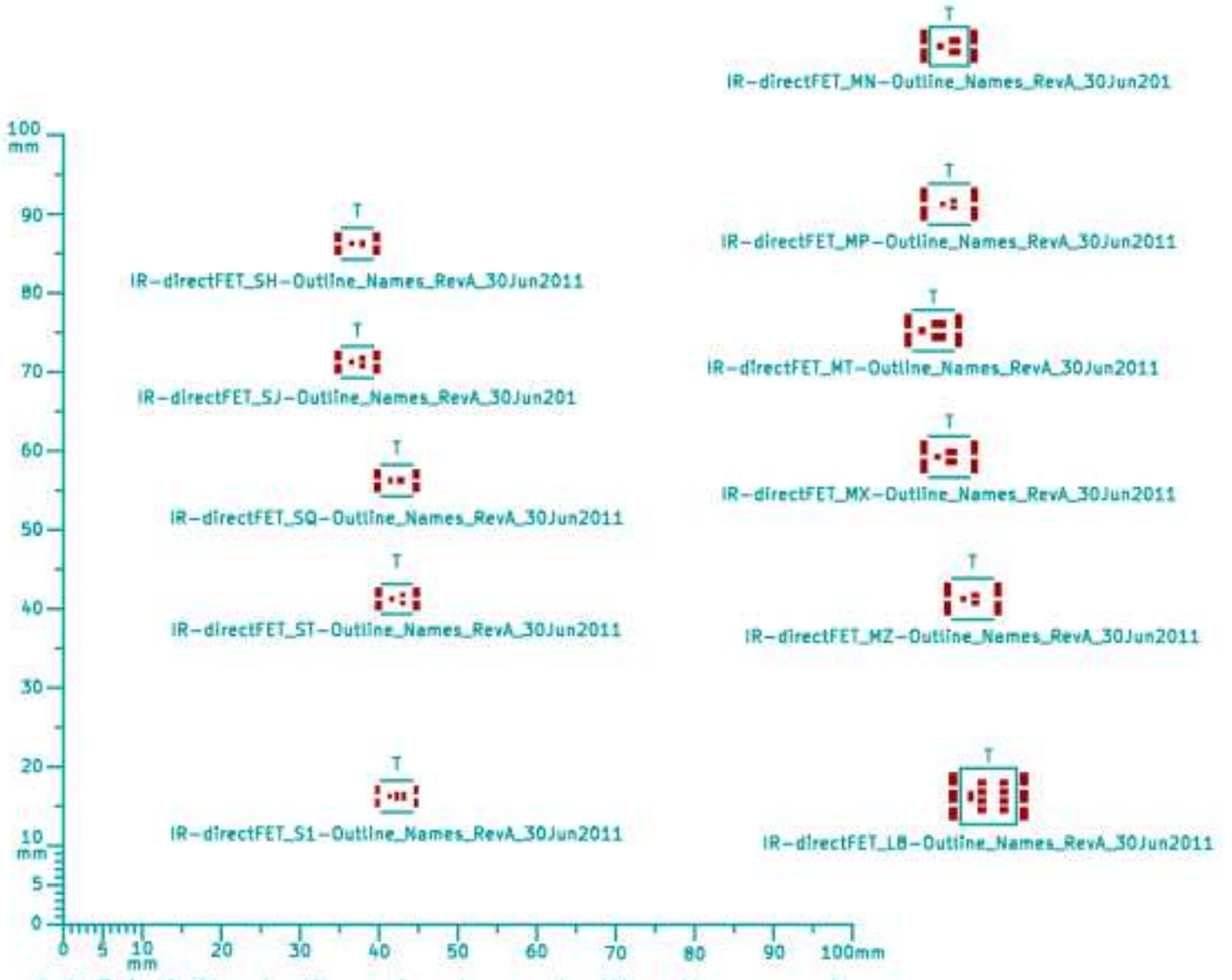

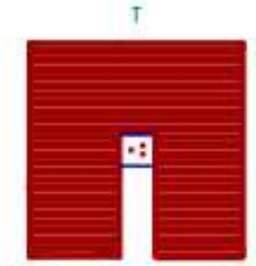

IR-directFET_ST-Outline_Numbers_SquareHeatSink_RevA_30Jun2011

Experimental footprint with copper plane

Heatsink

Footprints of International Rectifiers
directFet Transistor series.

SH-Outline	MN-Outline
SJ-Outline	NP-Outline
SQ-Outline	MT-Outline
ST-Outline	MX-Outline
S1-Outline	MZ-Outline
	LB-Outline

"directFET" is a trademark of International Rectifier.
Those housings are proprietary, so be careful of using them.

Those footprints are untested yet!
So be nice and make a report to bernd.wiebus@gmx.de
and tell about your experiences!

For suggestions
and improvements
please contact
me at
bernd.wiebus@gmx.de

1:1 Print! Check dimension by controlling the gauge!

KiCAD Footprints of some International Rectifier directFED Transistors from the file IR-directFET_Packages_RevA_30Jun2011.mod .

Author: Bernd Wiebus / Uedem / Germany / 02Jul2011

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File: IR-directFET_Packages_RevB.kicad_pcb		
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KiCad E.D.A.		Id: 1/1